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A superior primer on software testing and quality assurance, from integration to execution and automation This important new work fills the pressing need for a user-friendly text that aims to provide software engineers, software quality professionals, software developers, and students with the fundamental developments in testing theory and common testing practices. Software Testing and Quality Assurance: Theory and Practice equips readers with a solid understanding of: Practices that support the production of quality software Software testing techniques Life-cycle models for requirements, defects, test cases, and test results Process models for units, integration, system, and acceptance testing How to build test

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teams, including recruiting and retaining test engineers Quality Models, Capability Maturity Model, Testing Maturity Model, and Test Process Improvement Model Expertly balancing theory with practice, and complemented with an abundance of pedagogical tools, including test questions, examples, teaching suggestions, and chapter summaries, this book is a valuable, self-contained tool for professionals and an ideal introductory text for courses in software testing, quality assurance, and software engineering.

Silicon technology now allows us to build chips consisting of tens of millions of transistors. This technology not only promises new levels of system integration onto a single chip, but also presents significant challenges to the chip designer. As a result, many ASIC developers and silicon vendors are re-examining their design methodologies, searching for ways to make effective use of the huge numbers of gates now available. These designers see current design tools and methodologies as inadequate for developing million-gate ASICs from scratch. There is considerable pressure to keep design team size and design schedules constant even as design complexities grow. Tools are not providing the productivity gains required to keep pace with the increasing gate counts available from deep submicron technology. Design reuse - the use of pre-designed and pre-verified

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cores - is the most promising opportunity to bridge the gap between available gate-count and designer productivity. Reuse Methodology Manual for System-On-A-Chip Designs, Second Edition outlines an effective methodology for creating reusable designs for use in a System-on-a-Chip (SoC) design methodology. Silicon and tool technologies move so quickly that no single methodology can provide a permanent solution to this highly dynamic problem. Instead, this manual is an attempt to capture and incrementally improve on current best practices in the industry, and to give a coherent, integrated view of the design process. Reuse Methodology Manual for System-On-A-Chip Designs, Second Edition will be updated on a regular basis as a result of changing technology and improved insight into the problems of design reuse and its role in producing high-quality SoC designs.

This book covers all you need to know to model and design software applications from use cases to software architectures in UML and shows how to apply the COMET UML-based modeling and design method to real-world problems. The author describes architectural patterns for various architectures, such as broker, discovery, and transaction patterns for service-oriented architectures, and addresses software quality attributes including maintainability, modifiability, testability, traceability, scalability, reusability, performance,

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availability, and security. Complete case studies illustrate design issues for different software architectures: a banking system for client/server architecture, an online shopping system for service-oriented architecture, an emergency monitoring system for component-based software architecture, and an automated guided vehicle for real-time software architecture. Organized as an introduction followed by several short, self-contained chapters, the book is perfect for senior undergraduate or graduate courses in software engineering and design, and for experienced software engineers wanting a quick reference at each stage of the analysis, design, and development of large-scale software systems.

Electronic design automation (EDA) is among the crown jewels of electrical engineering. Without EDA tools, today's complex integrated circuits (ICs) would be impossible. Doesn't such an important field deserve a comprehensive, in-depth, and authoritative reference? The Electronic Design Automation for Integrated Circuits Handbook is that reference, ranging from system design through physical implementation. Organized for convenient access, this handbook is available as a set of two carefully focused books dedicated to the front- and back-end aspects of EDA, respectively. What's included in the Handbook? EDA for IC System Design, Verification, and Testing This first installment

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examines logical design, focusing on system-level and micro-architectural design, verification, and testing. It begins with a general overview followed by application-specific tools and methods, specification and modeling languages, high-level synthesis approaches, power estimation methods, simulation techniques, and testing procedures. EDA for IC Implementation, Circuit Design, and Process Technology Devoted to physical design, this second book analyzes the classical RTL to GDS II design flow, analog and mixed-signal design, physical verification, analysis and extraction, and technology computer aided design (TCAD). It explores power analysis and optimization, equivalence checking, placement and routing, design closure, design for manufacturability, process simulation, and device modeling. Comprising the work of expert contributors guided by leaders in the field, the Electronic Design Automation for Integrated Circuits Handbook provides a foundation of knowledge based on fundamental concepts and current industrial applications. It is an ideal resource for designers and users of EDA tools as well as a detailed introduction for newcomers to the field.

Since the publication of the Institute of Medicine (IOM) report Clinical Practice Guidelines We Can Trust in 2011, there has been an increasing emphasis on assuring that clinical practice guidelines are

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trustworthy, developed in a transparent fashion, and based on a systematic review of the available research evidence. To align with the IOM recommendations and to meet the new requirements for inclusion of a guideline in the National Guidelines Clearinghouse of the Agency for Healthcare Research and Quality (AHRQ), American Psychiatric Association (APA) has adopted a new process for practice guideline development. Under this new process APA's practice guidelines also seek to provide better clinical utility and usability. Rather than a broad overview of treatment for a disorder, new practice guidelines focus on a set of discrete clinical questions of relevance to an overarching subject area. A systematic review of evidence is conducted to address these clinical questions and involves a detailed assessment of individual studies. The quality of the overall body of evidence is also rated and is summarized in the practice guideline. With the new process, recommendations are determined by weighing potential benefits and harms of an intervention in a specific clinical context. Clear, concise, and actionable recommendation statements help clinicians to incorporate recommendations into clinical practice, with the goal of improving quality of care. The new practice guideline format is also designed to be more user friendly by dividing information into modules on specific clinical questions. Each module has a consistent organization, which will assist users in finding clinically useful and

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relevant information quickly and easily. This new edition of the practice guidelines on psychiatric evaluation for adults is the first set of the APA's guidelines developed under the new guideline development process. These guidelines address the following nine topics, in the context of an initial psychiatric evaluation: review of psychiatric symptoms, trauma history, and treatment history; substance use assessment; assessment of suicide risk; assessment for risk of aggressive behaviors; assessment of cultural factors; assessment of medical health; quantitative assessment; involvement of the patient in treatment decision making; and documentation of the psychiatric evaluation. Each guideline recommends or suggests topics to include during an initial psychiatric evaluation. Findings from an expert opinion survey have also been taken into consideration in making recommendations or suggestions. In addition to reviewing the available evidence on psychiatry evaluation, each guideline also provides guidance to clinicians on implementing these recommendations to enhance patient care.

The experience of using and interacting with the newest Virtual Reality and computing technologies is profoundly affected by the

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extent to which we feel ourselves to be really 'present' in computer-generated and -mediated augmented worlds. This feeling of 'Presence', of "being inside the mediated world", is key to understanding developments in applications such as interactive entertainment, gaming, psychotherapy, education, scientific visualisation, sports training and rehabilitation, and many more. This edited volume, featuring contributions from internationally renowned scholars, provides a comprehensive introduction to and overview of the topic of mediated presence - or 'tele-presence' - and of the emerging field of presence research. It is intended for researchers and graduate students in human-computer interaction, cognitive science, psychology, cyberpsychology and computer science, as well as for experienced professionals from the ICT industry. The editors are all well-known professional researchers in the field: Professor Giuseppe Riva from the Catholic University of Milan, Italy; Professor John Waterworth from Umeå University, Sweden; Dianne Murray, an HCI Consultant and editor of the journal "Interacting with Computers".

This fifth edition continues to build upon previous issues with its hands-on approach to systems analysis and design with an even more in-depth focus on the core set of skills that all analysts must possess. Dennis continues to capture the experience of developing and analysing

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systems in a way that readers can understand and apply and develop a rich foundation of skills as a systems analyst.

This book starts with an introduction to process modeling and process paradigms, then explains how to query and analyze process models, and how to analyze the process execution data. In this way, readers receive a comprehensive overview of what is needed to identify, understand and improve business processes. The book chiefly focuses on concepts, techniques and methods. It covers a large body of knowledge on process analytics - including process data querying, analysis, matching and correlating process data and models - to help practitioners and researchers understand the underlying concepts, problems, methods, tools and techniques involved in modern process analytics. Following an introduction to basic business process and process analytics concepts, it describes the state of the art in this area before examining different analytics techniques in detail. In this regard, the book covers analytics over different levels of process abstractions, from process execution data and methods for linking and correlating process execution data, to inferring process models, querying process execution data and process models, and scalable process data analytics methods. In addition, it provides a review of commercial process analytics tools and their practical

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applications. The book is intended for a broad readership interested in business process management and process analytics. It provides researchers with an introduction to these fields by comprehensively classifying the current state of research, by describing in-depth techniques and methods, and by highlighting future research directions. Lecturers will find a wealth of material to choose from for a variety of courses, ranging from undergraduate courses in business process management to graduate courses in business process analytics. Lastly, it offers professionals a reference guide to the state of the art in commercial tools and techniques, complemented by many real-world use case scenarios.

This book provides a practical guide for engineers doing low power System-on-Chip (SoC) designs. It covers various aspects of low power design from architectural issues and design techniques to circuit design of power gating switches. In addition to providing a theoretical basis for these techniques, the book addresses the practical issues of implementing them in today's designs with today's tools.